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Nallatech Announces Strategic Agreement with Intel To Develop FSB FPGA Accelerator Modules and Design Tools

Baltimore, MD and Beijing, China, April 17th, 2007. Nallatech, the leader in high-performance FPGA computing solutions, today announced at the Intel Developers Forum that it has signed an agreement with Intel to develop socket-based accelerator modules which support the Intel® QuickAssist Technology accelerator strategy.

Nallatech is working with Intel and Xilinx to develop and deliver FSB FPGA accelerator modules and design tools to the advanced server market during 2007. These products are compatible with Intel's latest server platforms and utilize Xilinx's 65nm Virtex™-5 FPGA architecture. As part of this relationship, Nallatech will deliver high level design tools conforming to the Intel® QuickAssist Technology Accelerator Abstraction Layer (AAL) enabling support for ANSI C based third party tool flows from both existing and new partners.

"We are extremely excited about the engagement with Intel as it allows us to bring more than a decade of experience in FPGA computing and match it with the rapidly increasing demand for accelerated processing capabilities by customers in the advanced server market," said Craig Anderson, CEO of Nallatech. "The flexibility and performance of FPGAs coupled with high speed direct access to the server processors allows users to significantly accelerate the performance of computationally intensive algorithms, thus reducing compute time and power consumption".

"Nallatech has extensive experience in building products that utilize FPGAs as accelerators and we are delighted to join with this industry innovator in developing solutions for Intel's industry-leading acceleration platforms," said Dr. Dileep Bhandarkar, Director of Advanced Architectures for Intel Corporation. "The combination of Nallatech's FPGA computing expertise with Intel's advanced server platforms, together with our shared cross market application knowledge, will create a powerful solution for addressing the most demanding computing problems".

About Nallatech

Nallatech is the leader in high performance FPGA computing, helping address the challenges of delivering improved price/performance, increased performance-density and reduced power consumption. Nallatech addresses these needs, enabling applications to be accelerated using FPGA compute technology. Built with the aim of simplifying the design and creation of scalable clusters, Nallatech provides the most flexible FPGA computing solutions available on the market today. Products are available as integrated solutions as well as expansion cards suitable for most cluster systems. Headquartered in Glasgow, Scotland, its wholly-owned subsidiary Nallatech Inc. has U.S. Headquarters in Baltimore and Regional offices in San Jose, Los Angeles, Philadelphia and Boston. Nallatech has six operating locations worldwide and international partner sales offices in seven countries.

For further information please visit – www.nallatech.com